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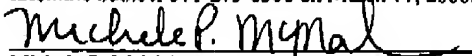
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**Re:** *Tae-joong Song, Memory Devices Having Bit Line Precharge Circuits with Off Current Precharge Control and Associated Bitline Precharge Methods, Serial No. 10/783,481, filed February 20, 2004*

Attached is a Response, Replacement Sheets for Figs. 2 and 5 and Statement of Accuracy of a Translation for the above-identified application.

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